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Electronic Version v1.1

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Title of Invention	MULTI-PORT MEMORY ARCHITECTURE
Application Number :	
Date :	
First Named Applicant:	Toshiaki Kirihata
Confirmation Number:	
Attorney Docket Number:	FIS920030119US1

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Documents being submitted:	Files
us-assignment	FIS920030119US1-usassn.xml
	us-assignment.xsl
	us-assignment.dtd
	FIS920030119US1Assignment1.tif
	FIS920030119US1Assignment2.tif
	FIS920030119US1Assignment3.tif
us-request	FIS920030119US1-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	FIS920030119US1-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
application-body	FIS920030119US1-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	FIS920030119US1Drawing1.tif
	FIS920030119US1Drawing2.tif
	FIS920030119US1Drawing3.tif
	FIS920030119US1Drawing4.tif
	FIS920030119US1Drawing5.tif
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	FIS920030119US1Drawing7.tif
	FIS920030119US1Drawing8.tif
us-declaration	FIS920030119US1Declaration1.tif

us-declaration	FIS920030119US1Declaration2.tif		
us-declaration	FIS920030119US1Declaration3.tif		
us-declaration	FIS920030119US1Declaration4.tif		
Comments			
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